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STATEMENT UNDER 37 CFR 3.73(b)		
Applicant/Patent Owner: Craig Bingert et al.		
Application No./Patent No.: Not yet assigned	iled/ssue Date:	6/20/03
Entitled: Integrated Circuit And Associated Design Method Using Spare Gate Islands		
Lattice Semiconductor Corporation , a Delaware Corporation		
(Name of Assignee)		poration, partnership, university, government agency, etc.)
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The undersigned (whose title is supplied below) is author		
<u> </u>		flark L. Becker
'Date 503-268-8629	11	Typed of printed name
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